



Serial No. 10/634,174

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes Examiner: Joannie A. Garcia
Serial No.: 10/634,174 Group Art Unit: 2823
Filed: August 5, 2003 Docket: 1303.102US1
Title: STRAINED SI/SIGE/SOI ISLANDS AND PROCESSES OF MAKING SAME

COMMUNICATION CONCERNING RELATED APPLICATIONS

MS RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11318124	December 23, 2005	1303.095US3	STRAINED SEMICONDUCTOR BY WAFER BONDING WITH MISORIENTATION
11432578	May 11, 2006	1303.100US3	MICROMECHANICAL STRAINED SEMICONDUCTOR BY WAFER BONDING
11475798	June 27, 2006	1303.109US2	STRAINED SEMICONDUCTOR BY FULL WAFER BONDING

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of July, 2006.

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Name

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